

Notice of References Cited	Application/Control No. 10/706,931	Applicant(s)/Patent Under Reexamination NAKATA, KENICHI	
	Examiner Harry Behm	Art Unit 2838	Page 1 of 1

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*	B	US-6,288,524	09-2001	Tsujimoto, Hirokazu	323/285
*	C	US-6,865,682	03-2005	Talbot et al.	713/300
*	D	US-7,098,631	08-2006	Cohen, Isaac	323/222
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	L	US-			
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NON-PATENT DOCUMENTS

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	U	WO 00/79370 Gerry Talbot, Microprocessor module with integrated voltage regulators, WIPO, December 28, 2000, pages 1-16.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.